

17707 U.S. PTO
102703

Page 1

PATENT

Docket No.: JCLA9730

Date: October 27, 2003

COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

ATTENTION: APPLICATION BRANCH

Sir:

22581 U.S. PTO
10/695630

102703

This is a request for filing a continuation application under 37 CFR 1.53 (b) as a Continuation-in-Part application of prior application No. 09/798,654, filed on March 05, 2001 and Continuation-in-Part application of application No. 10/055,580, filed on January 22, 2002.

Transmitted herewith for filing is the patent application of

Inventor(s): SHIH-HSIUNG LIN ;

MOU-SHIUNG LIN ;

For: METHOD OF ASSEMBLING CHIPS

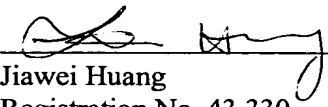
(X) This application claims the priority benefit of Taiwan application No. 91125126 filed on October 25, 2002.

Enclosed are:

- (X) Specification in FOURTY-TWO (42) pages.
- (X) THIRTY-TWO (32) sheets of drawings.
- (X) Applicant claims small entity status, see 37 CFR 1.27.
- (X) Return prepaid postcard.

Note:

THIS APPLICATION IS FILED UNSIGNED AND UNPAID


Jiawei Huang
Registration No. 43,330

Please send communications to:

J.C. Patents
4 Venture, Suite 250
Irvine, California 92618
Tel.: (949) 660-0761
Fax: (949)-660-0809

J.C. PATENTS, INC.
4 VENTURE, SUITE 250
IRVINE, CALIFORNIA 92618
(949) 660-0761
FAX: (949) 660-0809

Assistant Commissioner for Patents
P.O. Box 1450, Alexandria, VA 22313-1450

CERTIFICATE OF MAILING BY "EXPRESS MAIL"

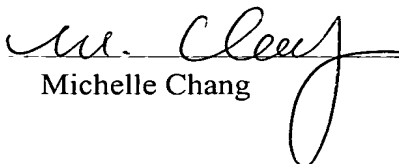
Docket No. : JCLA9730
Applicant(s) : Shih-Hsiung Lin, Mou-Shiung Lin
For : METHOD OF ASSEMBLING CHIPS
"Express Mail" : EV 338304384 US
Mailing Label No.

Date of Deposit : *October 27, 2003*

I hereby certify that the accompanying

Transmittal in Duplicate; Specification 42 pages, 32 sheets of drawings; Return Prepaid Postcard

are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and are addressed to the Assistant Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Michelle Chang